

**AMENDMENTS TO THE SPECIFICATION****IN THE SPECIFICATION:**

Please amend the Abstract of the Disclosure as indicated below, and replace it with the attached new sheet located at the end of this Preliminary Amendment.

**AMENDMENTS TO THE ABSTRACT**

A semiconductor storage device wherein storage of two bits is implemented in one transistor and that can be further miniaturized is provided. Two charge holding portions ~~(61, 62)~~, one on each sidewall of the gate electrode ~~(13)~~, are formed so as to be independent of the gate insulating film ~~(12)~~. Thereby, the memory function carried out by the charge holding portions ~~(61, 62)~~ and the transistor operation function carried out by the gate insulating film ~~(12)~~ are separated. The two charge holding portions ~~(61, 62)~~, formed on opposite sides of the gate electrode ~~(13)~~, are separated by the gate electrode ~~(13)~~ and, therefore, interference at the time of rewriting can be effectively suppressed. Accordingly, a semiconductor storage device wherein storage of two bits is implemented in one transistor and that is miniaturized is provided.